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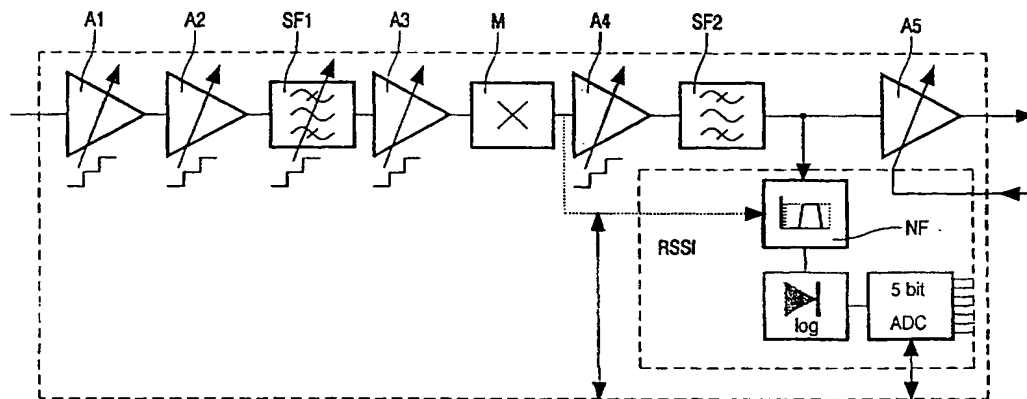
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(54) Title: **RECEIVER SIGNAL STRENGTH INDICATION**



(57) Abstract: A receiver signal strength indication circuit comprises circuitry (A1-A4) for discretely controlled amplifying an input signal, and circuitry (NF, log, ADC) coupled to an output of the amplifying circuitry (A1-A4) for furnishing a receiver signal strength indication.



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